

SFP-25SM31-02C

25Gbps SFP28 Transceiver, Single Mode, 2km Reach

Features

- Supports up to 25.78Gbps bit rates
- Hot-pluggable SFP+ footprint
- 1310nm DFB laser and PIN photodiode, Up to 2km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:
- Standard: 0 to +70°C



Applications

25GBASE-LR

Description

The SFP28 transceivers are high performance, cost effective modules supporting data rate of 25.78Gbps and 2km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

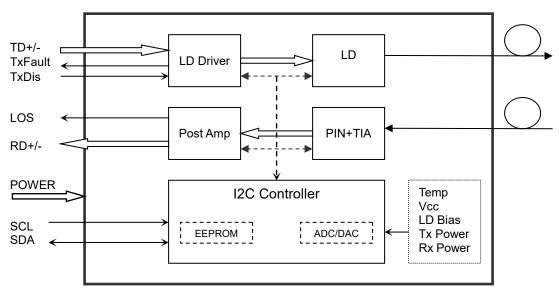
The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.

Internal CDR's Locking Modes

Bits 110.3 and bits 118.3 control the locking modes of the internal retimer or CDR, default Value is "1".

Bit 110.3 of A2h	Bit 118.3 of A2h	RX Data Rate	TX Data Rate	Status of RX CDR	Status of TX CDR
High/1	High/1	24.33G/25.78G	24.33G/25.78G	CDR select	CDR select
High/1	Low/0	24.33G/25.78G	9.95G/10.31G	CDR select	CDR bypass
Low/0	High/1	9.95G/10.31G	24.33G/25.78G	CDR bypass	CDR select
Low/0	Low/0	9.95G/10.31G	9.95G/10.31G	CDR bypass	CDR bypass





Transceiver functional diagram

Absolute Maximum Ratings

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Table 2- Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		+70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			400	mA
Data Rate			25.78		Gbps



Optical and Electrical Characteristics

Table 3- Optical and Electrical Characteristics

Parai	meter	Symbol	Min	Typical	Max	Unit	Notes
			Transmi	tter			
Centre V	Vavelength	λс	1270	1310	1350	nm	
Spectral W	idth (-20dB)	Δλ			1	nm	
Side-Mode Su	uppression Ratio	SMSR	30	-		dB	
Average C	Output Power	P _{out}	-7		2	dBm	1
Extinct	tion Ratio	ER	3.5			dB	
Data Input Sv	wing Differential	Vin	180		850	mV	2
Input Differer	ntial Impedance	Z _{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
I A Disable	Enable		0		0.8	V	
TV F!t	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receiv	er			
Centre V	Vavelength	λς	1260		1600	nm	
Receiver	Sensitivity				-13.3	dBm	3
Receive	r Overload				2	dBm	3
LOS	LOS De-Assert				-15	dBm	
LOS	LOS Assert		-30			dBm	
LOS H	LOS Hysteresis		0.5			dB	
Data Output S	Swing Differential	V _{out}	300		900	mV	4
	22	High	2.0		Vcc	V	
L	.OS	Low			0.8	V	

Notes:

- 1. The optical power is launched into SMF.
- 2. PECL input, internally AC-coupled and terminated.
- 3. Measured with a PRBS 2³¹-1 test pattern @25.78Gps, BER ≤5×10⁻⁵.
- 4. Internally AC-coupled.



Diagnostics

Table 4- Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-7 to 2	dBm	±3dB	Internal
RX Power	-14 to +2	dBm	±3dB	Internal

Timing and Electrical

Table 5- Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			2	ms
Tx Disable Assert Time	t_off			100	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

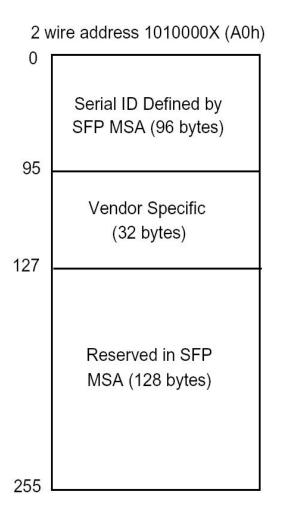


Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.

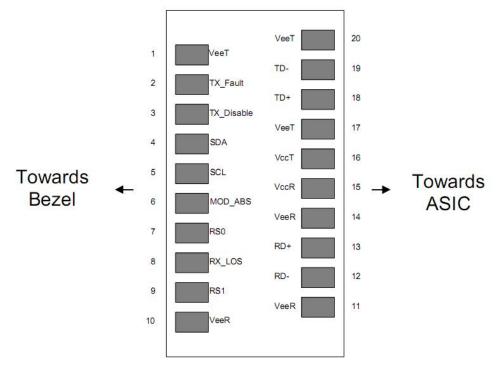


	wire address 1010001X (A2h))
55	Alarm and Warning Thresholds (56 bytes)	
95	Cal Constants (40 bytes)	
	Real Time Diagnostic Interface (24 bytes)	
119 127	Vendor Specific (8 bytes)	
	User Writable EEPROM (120 bytes)	
247		
255	Vendor Specific (8 bytes)	



Pin Assignment

Pin Diagram



Pin Descriptions

Table 6- Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V _{EER}	Receiver ground	1	
15	V _{CCR}	Receiver Power Supply	2	



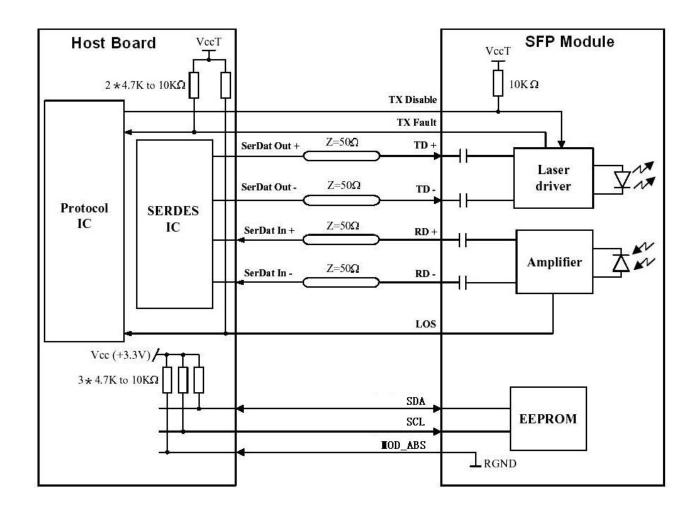
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V _{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

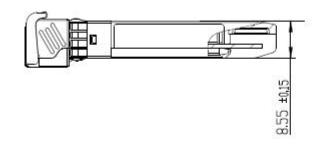
- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

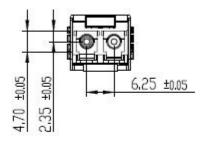
Recommended Interface Circuit

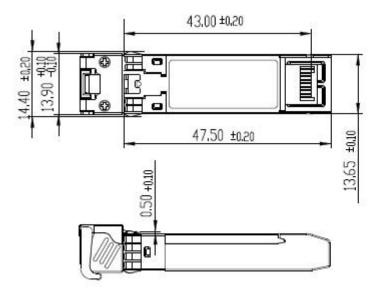




Mechanical Dimensions







Ordering information

Table 7- Ordering information

Part Number	Product Description
SFP-25SM31-02C	1310nm, 25.78Gbps, LC, 2km, 0°C~+70°C, with DDM

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