

#### SFP-8FMM85-3HC

8.5Gbps SFP+ Transceiver, Multi Mode, 300m Reach

#### **Features**

- Supports up to 8.5Gbps bit rates
- Hot-pluggable SFP+ footprint
- 850nm VCSEL laser and PIN photodiode, Up to 300m for OM3-MMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:

Standard: 0 to +70°C Industrial: -40 to +85°C

#### **Applications**

- Tri-Rate 2.125/4.25/8.5Gbps Fiber Channel
- Wireless CPRI and OBSAI
- Other Optical links

#### **Description**

The SFP+ transceivers are high performance, cost effective modules supporting data rate of 8.5Gbps.

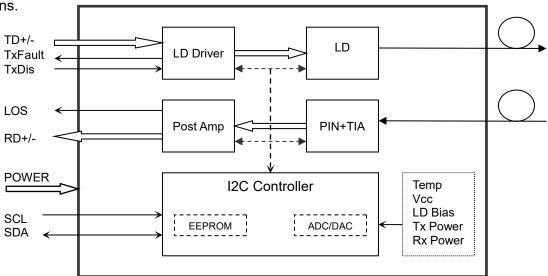
Fiber type	Minimum modal bandwidth @ 850 nm (MHz•km)	Operating range (meters)
62.5 μm MMF	160	2 to 26
	200	2 to 33
50 μm MMF	400	2 to 66
	500	2 to 82
	2000	2 to 300

The transceiver consists of three sections: a VCSEL laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.





The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.



Transceiver functional diagram

## **Absolute Maximum Ratings**

**Table 1 - Absolute Maximum Ratings** 

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

## **Recommended Operating Environment**

**Table 2 - Recommended Operating Environment** 

Parameter	, <b>.</b>	Symbol	Min	Typical	Max	Unit
	Standard		0		+70	°C
Operating Case Temperature	Extended	Tc	-20		+80	°C
	Industrial		-40		+85	°C
Power Supply Voltage		Vcc	3.135	3.30	3.465	V
Power Supply Current		Icc			260	mA
Data Rate			1.0		8.5	Gbps



## **Optical and Electrical Characteristics**

SFP-8FMM85-3HC: (VCSEL and PIN, 300m Reach)

**Table 3 - Optical and Electrical Characteristics** 

Parai	meter	Symbol	Min	Typical	Max	Unit	Notes
			Transmi	tter			
Centre V	Vavelength	λс	840	850	860	nm	
Spectral W	ridth (RMS)	Δλ			0.65	nm	
Side-Mode Su	uppression Ratio	SMSR	-	-	-	dB	
Average C	Output Power	Pout	-6.0		-0.5	dBm	1
Extinc	ion Ratio	ER	3.0			dB	
Data Input Sv	wing Differential	V <sub>IN</sub>	180		950	mV	2
Input Differer	ntial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
TA Disable	Enable		0		0.8	V	
TV Fault	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receiv	er			
Centre V	Vavelength	λс	840	850	860	nm	
Receive	Sensitivity				-10.5	dBm	3
Receive	r Overload		0.5			dBm	3
LOS	LOS De-Assert				-12	dBm	
LOS Assert		LOSA	-22			dBm	
LOS Hysteresis			0.5		4	dB	
Data Output S	Data Output Swing Differential		500	700	900	mV	4
	LOS		2.0		Vcc	V	
L					0.8	V	

- 1. The optical power is launched into MMF.
- PECL input, internally AC-coupled and terminated.
  Measured with a PRBS 2<sup>31</sup>-1 test pattern @8500Mbps, BER ≤1×10<sup>-12</sup>.
- 4. Internally AC-coupled.



# **Diagnostics**

**Table 4 – Diagnostics Specification** 

Parameter	Range	Unit	Accuracy	Calibration
	0 to +70			
Temperature	-20 to +80	°C	±3°C	Internal
	-40 to +85			
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 15	mA	±10%	Internal
TX Power	-6.0 to -0.5	dBm	±3dB	Internal
RX Power	-16 to -1	dBm	±3dB	Internal

## **Timing and Electrical**

**Table 5 - Timing and Electrical** 

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V <sub>H</sub>	2		Vcc	V
MOD_DEF (0:2)-Low	V <sub>L</sub>			0.8	V

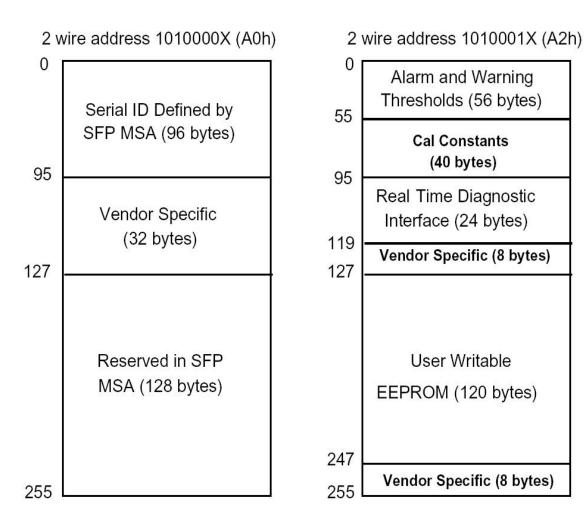


### **Digital Diagnostic Memory Map**

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

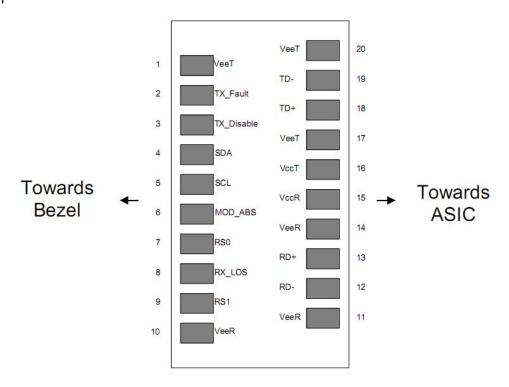
The digital diagnostic memory map specific data field defines as following.





## **Pin Assignment**

Pin Diagram



## **Pin Descriptions**

**Table 6- Pin Descriptions** 

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V <sub>EER</sub>	Receiver ground	1	
11	V <sub>EER</sub>	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4



13	RD+	Received Data Out	3	Note 4
14	V <sub>EER</sub>	Receiver ground	1	
15	V <sub>CCR</sub>	Receiver Power Supply	2	
16	V <sub>CCT</sub>	Transmitter Power Supply	2	
17	V <sub>EET</sub>	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V <sub>EET</sub>	Transmitter Ground	1	

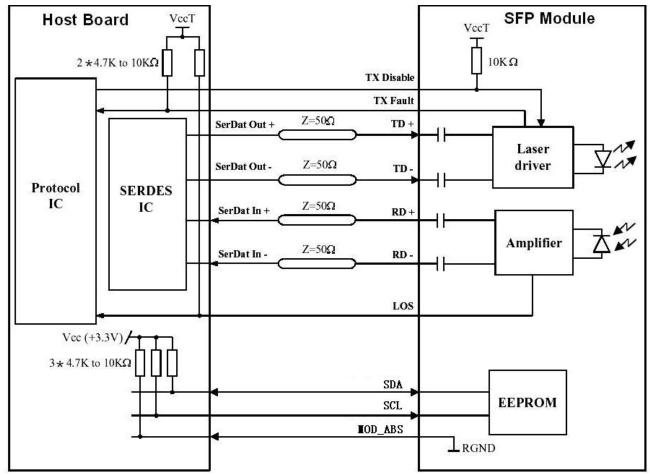
#### Notes

Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

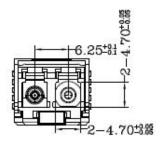
#### **Recommended Interface Circuit**

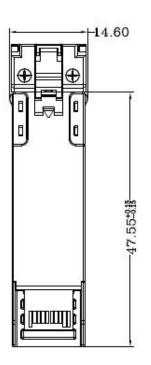


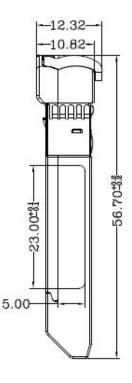


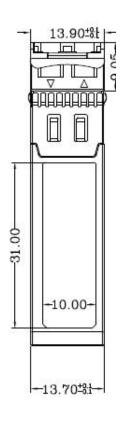
**Mechanical Dimensions** 











## **Ordering information**

**Table 7- Ordering information** 

Part Number	Product Description	
SFP-8FMM85-3HCC	850nm, 8.5Gbps, LC, 300m, 0°C~+70°C	
SFP-8FMM85-3HCI	850nm, 8.5Gbps, LC, 300m, -40°C~+85°C	

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