

GBIC-24SM31-40C

1.25Gbps GBIC Optical Transceiver, 40km Reach

Features

- Dual data-rate of 1.25Gbps/1.0625Gbps operation
- 1310nm DFB laser and PIN photodetector for 40km transmission
- Duplex SC optical interface
- Standard serial ID information compatible with SFF-8053
- +3.3V/5Vsingle power supply
- RoHS Compliant
- Operating case temperature: 0 to +70℃



Applications

- Switch to Switch interface
- Switched backplane applications
- Router/Server interface
- Other optical transmission systems

Description

The GBIC transceiver is high performance, cost effective module supporting dual data-rate of 1.25Gbps/1.0625Gbps and from 40km transmission distance with SMF.

The transceiver consists of two sections: The transmitter section incorporates a DFB laser. And the receiver section consists of a PIN photodiode integrated with a trans-impedance preamplifier (TIA). All modules satisfy class I laser safety requirements.

The optical output can be disabled by a TTL logic high-level input of Tx Disable. Tx Fault is provided to indicate degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver.

The standard serial ID information Compatible with GBIC MSA describes the transceiver's capabilities, standard interfaces, manufacturer and other information. The host equipment can access this information via the two-wire serial CMOS EEPROM protocol. For further information, please refer to SFF-8053



Absolute Maximum Ratings

Stress in excess of the maximum absolute ratings can cause permanent damage to the module.

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min	Typical	Max	Unit
Maximum Supply Voltage	Vcc	0.5	-	4.5	V
Storage Temperature	Ts	-40	-	100	$^{\circ}$
Relative Humidity	Rн	0	-	+85	%

Recommended Operating Conditions

Table2 - Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit		
Operating Cas	se Temperature	Standard	Tc	0	-	+70	$^{\circ}$	
Power Supply	Voltage		Vcc	3.1		5.5	V	
Power Supply	Current		Icc			300	mA	
Data Data	Gigabit Ethernet				1.25		Chan	
Data Rate	Fibre Channel				1.0625		Gbps	

Optical and Electrical Characteristics

GBIC-24SM31-40C: (1310nm DFB and PIN, 40km)

Table3 - Optical and Electrical Characteristics (Operating case temperature TC=25℃, VCC=3.3V)

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Pa	rameter	Symbol	Min.	Typical	Max.	Unit	Notes
			Transmi	tter			
Centre	Wavelength	λc	1260	1310	1360	nm	
Spectral	Width (-20dB)	σ			1	nm	
Side Mode S	Suppression Ratio	SMSR	30			dB	
Average	Output Power	P _{0ut}	-5		0	dBm	1
Extin	ction Ratio	ER	9			dB	
Output	t Optical Eye	IEEE 802.3z and ANSI Fibre Channel compatible		2			
Data Input Swing Differential		Vin	300		1860	mV	3
Input Differ	ential Impedance	ce Zin 90		100	110	Ω	
TV D: 11	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
T) (F 1)	Fault		2.0		Vcc+0.3	V	
TX Fault	Normal		0		0.8	V	
		<u>'</u>	Receiv	er	1	<u> </u>	•
Centre	Wavelength	λc	1260		1580	nm	



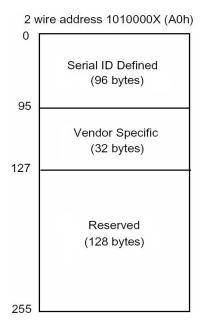
Receiver Sensitivity			-22	dBm	4
Receiver Overload		-3		dBm	4
Optical Path Penalty			1	dB	5
LOS De-Assert	LOSD		-23	dBm	
LOS Assert	LOSA	-30		dBm	
LOS Hysteresis		1	4	dB	
Data Output Swing Differential	Vout	370	1800	mV	6

Notes:

- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 27-1 test pattern @1250Mbps.
- 3. PECL input, internally AC coupled and terminated.
- 4. Measured with a PRBS 2⁷-1 test pattern @1250Mbps, BER ≤1×10⁻¹².
- 5. Measured with a PRBS 2⁷-1 test pattern @1250Mbps, over 20km G.652 SMF, BER ≤1×10⁻¹².
- 6. Internally AC coupled.

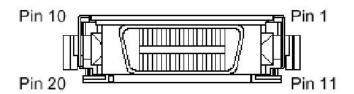
EEPROM Section

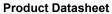
The SFF-8053 defines a 256-byte memory map in EEPROM describing the transceiver's capabilities, standard interfaces, manufacturer, and other information, which is accessible over a 2 wire serial interface at the 8-bit address 1010000X (A0h).



Pin Assignment

Pin Diagram







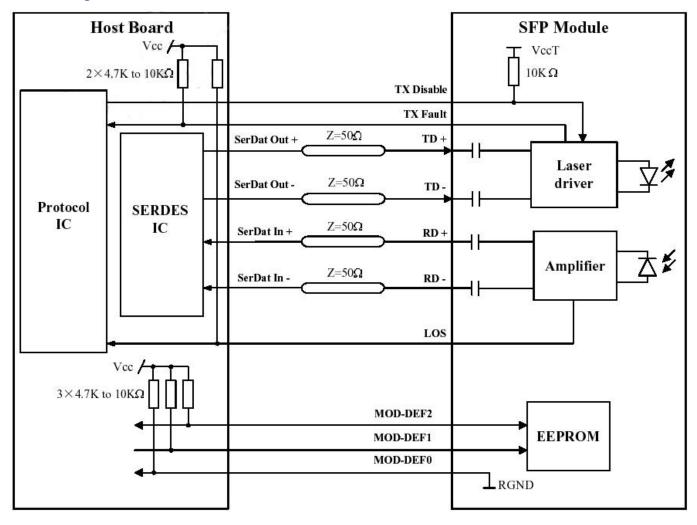
Pin Descriptions

Table 5- Pin Descriptions

Pin Name	Pin#	Name/Function	Signal Specification			
	Receiver signals					
RGND	2,3,11,14	Receiver Ground (may be connected sith TGND in GBIC)	Ground,to GBIC			
VDDR	15	Receiver+3.3/5 volt (may be connected with VDDT in GBIC)	Power,to GBIC			
-RX_DAT	12	Receive Data, Differential PECL	High speed serial.from GBIC			
+RX_DAT	13	Receive Data,Differential PECL	High speed serial.from GBIC			
RX_LOS	1	Receiver Loss of Signal,logic high,open collector compatible,4.7k to 10k Ω pull up to VDDT on host	Low speed,from GBIC			
	Transmitter signals					
TGND	8,9,17,20	Transmitter Ground (may be connected with RGND internally)	Ground,to GBIC			
VDDT	16	Transmitter +3.3/5 volt (may be connected with VDDR in GBIC)	Power,to GBIC			
-TX_DAT	18	Transmit Data, Differential PECL	High speed serial,to GBIC			
+TX_DAT	19	Transmit Data, Differential PECL	High speed serial,to GBIC			
TX_DISABLE	7	Transmitter Disable,logic high,open collector Compatible,4.7k to 10k Ω pull up to VDDT on GBIC	Low speed,to GBIC			
TX_FAULT	10		Low speed,from GBIC			
	Control signals					
MOD_DEF(0)	4	TTL low,output	Please reference			
MOD_DEF(1)	5	SCL serial clock signal,input	SFF-8053,Annex D;			
MOD_DEF(2)	6	SDA serial data signal,input/output	Module definition"4"			

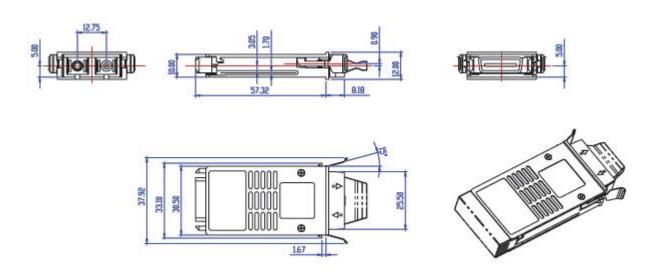


Block Diagram of Transceiver





Mechanical Dimensions



Ordering information

Table 7- Ordering information

Part Number	Product Description
GBIC-24SM31-40C	1310nm, 1.25Gbps, 40km, 0°C~+70°C

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